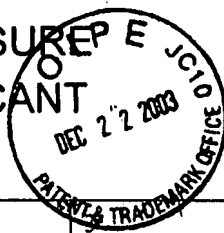


Substitut for Form 1449/PTO

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)



C mplete if Known

Application Number	10/676,889
Filing Date	September 30, 2003
First Named Inventor:	Shih-wei Liao et al.
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	42P16806

Sheet	1	of
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^o
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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Sheet	2	of	3

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NON PATENT LITERATURE DOCUMENTS			
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↓		JERRY HUCK, et al., "Introducing The IA-64 Architecture," IEEE MICRO, September-October 2000, pages 12-23, IEEE, Los Alamitos, CA, USA.	
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↓		STEVE S. W. LIAO, et al., "Post-Pass Binary Adaptation for Software-Based Speculative Precomputation," PLDI'02, June 17-19, 2002, Berlin, Germany, pages 117-128.	
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↓		DONGKEUN KIM and DONALD YEUNG, "Design and Evaluation of Compiler Algorithms for Pre-Execution," ASPLOS X, 10/02, San Jose, CA, USA, pages 159-170.	
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↓		HIRALAL AGRAWAL and JOSEPH R. HORGAN, "Dynamic Program Slicing," Proceedings of the ACM SIGPLAN'90 Conference on Programming Language Design and Implementation, White Plains, New York, June 20-22, 1990, pages 246-256.	
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/JM/		CRAIG ZILLES and GURINDAR SOHI, "Execution-based Prediction Using Speculative Slices," IEEE, 2001, pages 2-13.	
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		XINMIN TIAN, et al., "Intel® OpenMP C++/Fortran Compiler for Hyper-Threading Technology: Implementation and Performance," Intel Technology Journal Q1 2002. Vol. 6 Issue 1, 11 pages, Intel Corporation. Downloaded from http://developer.intel.com/ .	
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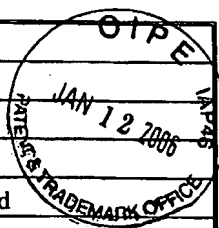
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/JM/		DORAI: "Optimizing SMT Processors for High Single-Thread Performance", The Journal of Instruction-Level Parallelism, vol. 5, April 2003, XP002348824, URL: http://www.jilp.org/vol15/v5paper3.pdf				
/JM/		LIAO: "Post-pass binary adaptation for software-based speculative precomputation", ACM SIGPLAN NOTICES, ACM, ASSOCIATION FOR COMPUTING MACHINERY, NEW YORK, NY, US vol. 37, no. 5, May 2002, pp. 117-128, XP002302652				
/JM/		XINMIN TIAN: "Intel @ Open MP C++/Fortran Compiler for Hyper-Threading Technology: Implementation and Performance" INTEL TECHNOLOGY JOURNAL INTEL CORP USA, vol. 6, no.1, February 14, 2002, pp. 36-46, XP002348825, ISSN: 1535-864X, URL: ftp://download.intel.com/technology/itj/2002/volume06issue01/art04_fortrancompiler/vol6iss1_art04.pdf				



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